

Notice of References Cited	Application/Control No. 09/928,848	Applicant(s)/Patent Under Reexamination MANDELL ET AL.	
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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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↑	B	US-6,182,258	01-2001	Hollander, Yoav	714/739
	C	US-5,933,356	08-1999	Rostoker et al.	703/15
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	E	US-5,222,030	06-1993	Dangelo et al.	703/15
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	G	US-6,782,511	08-2004	Frank et al.	716/9
	H	US-5,572,437	11-1996	Rostoker et al.	703/13
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*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Heirarchial Model Partitioning for Parallel VLSI-Simulation Using Evolutionary Algorithms Improved By Superpositions of Partitions" by Haupt R. et al; Proc. 5th European Conr. on Intell. Techn. and Soft Comp. (EUFIT'97), p.804-808, 1997.
✗	V	"Computer-Aided Partitioning of Behavioral Hardware", McFarland, M.C., S.J. This paper appears in: IEEE, Design Automation Conference Proceedings 1983, 20th, Pages:472-478
✗	W	"Assignment Decision Diagram and its Uses in High-level Synthesis", V. Chaiyakul and D.D. Gajski, Technical Report #9~103, University of California Irvine, October 1992.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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